

# 74HC3G34; 74HCT3G34

Triple buffer gate

Rev. 7 — 11 June 2018

Product data sheet

## 1 General description

The 74HC3G34; 74HCT3G34 is a triple buffer. Inputs include clamp diodes. This enables the use of current limiting resistors to interface inputs to voltages in excess of  $V_{CC}$ .

## 2 Features and benefits

- Wide supply voltage range from 2.0 V to 6.0 V
- Input levels:
  - For 74HC3G34: CMOS level
  - For 74HCT3G34: TTL level
- Complies with JEDEC standard no. 7 A
- Symmetrical output impedance
- High noise immunity
- Low-power dissipation
- Balanced propagation delays
- Multiple package options
- ESD protection:
  - HBM JESD22-A114E exceeds 2000 V
  - MM JESD22-A115-A exceeds 200 V
- Specified from -40 °C to +85 °C and -40 °C to +125 °C

## 3 Ordering information

Table 1. Ordering information

| Type number | Package           |        |                                                                                         |          |
|-------------|-------------------|--------|-----------------------------------------------------------------------------------------|----------|
|             | Temperature range | Name   | Description                                                                             | Version  |
| 74HC3G34DP  | -40 °C to +125 °C | TSSOP8 | plastic thin shrink small outline package; 8 leads; body width 3 mm; lead length 0.5 mm | SOT505-2 |
| 74HCT3G34DP |                   |        |                                                                                         |          |
| 74HC3G34DC  | -40 °C to +125 °C | VSSOP8 | plastic very thin shrink small outline package; 8 leads; body width 2.3 mm              | SOT765-1 |
| 74HCT3G34DC |                   |        |                                                                                         |          |

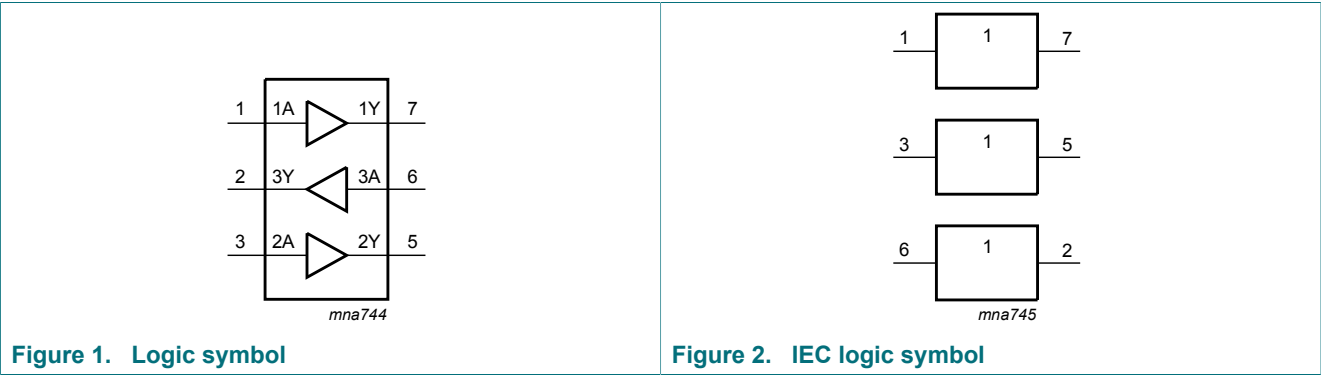
4 Marking

Table 2. Marking

| Type number | Marking code <sup>[1]</sup> |
|-------------|-----------------------------|
| 74HC3G34DP  | H34                         |
| 74HCT3G34DP | T34                         |
| 74HC3G34DC  | P34                         |
| 74HCT3G34DC | U34                         |

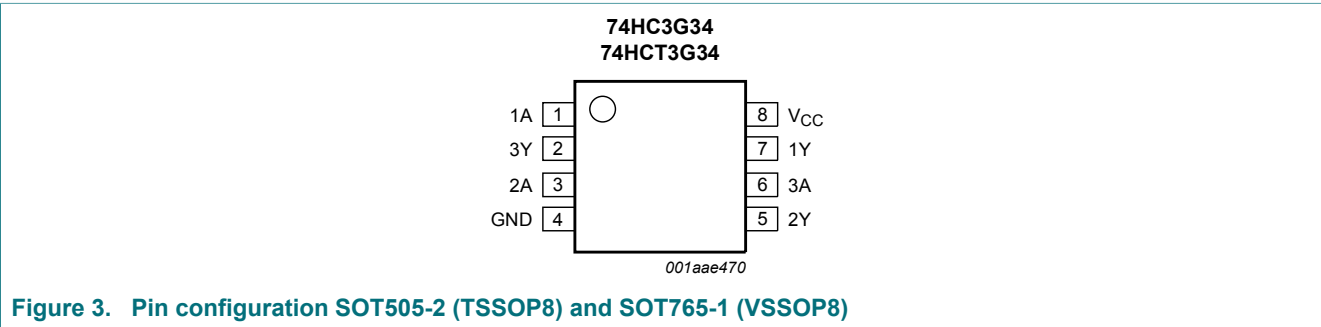
[1] The pin 1 indicator is located on the lower left corner of the device, below the marking code.

5 Functional diagram



6 Pinning information

6.1 Pinning



## 6.2 Pin description

Table 3. Pin description

| Symbol          | Pin     | Description    |
|-----------------|---------|----------------|
| 1A, 2A, 3A      | 1, 3, 6 | data input     |
| 1Y, 2Y, 3Y      | 7, 5, 2 | data output    |
| GND             | 4       | ground (0 V)   |
| V <sub>CC</sub> | 8       | supply voltage |

## 7 Functional description

Table 4. Function table

H = HIGH voltage level; L = LOW voltage level.

| Input | Output |
|-------|--------|
| nA    | nY     |
| L     | L      |
| H     | H      |

## 8 Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134). Voltages are referenced to GND (ground = 0 V).

| Symbol           | Parameter                | Conditions                                                              | Min  | Max  | Unit |
|------------------|--------------------------|-------------------------------------------------------------------------|------|------|------|
| V <sub>CC</sub>  | supply voltage           |                                                                         | -0.5 | +7.0 | V    |
| I <sub>IK</sub>  | input clamping current   | V <sub>I</sub> < -0.5 V or V <sub>I</sub> > V <sub>CC</sub> + 0.5 V [1] | -    | ±20  | mA   |
| I <sub>OK</sub>  | output clamping current  | V <sub>O</sub> < -0.5 V or V <sub>O</sub> > V <sub>CC</sub> + 0.5 V [1] | -    | ±20  | mA   |
| I <sub>O</sub>   | output current           | V <sub>O</sub> = -0.5 V to (V <sub>CC</sub> + 0.5 V)                    | -    | ±25  | mA   |
| I <sub>CC</sub>  | quiescent supply current |                                                                         | -    | 50   | mA   |
| I <sub>GND</sub> | ground current           |                                                                         | -50  | -    | mA   |
| T <sub>stg</sub> | storage temperature      |                                                                         | -65  | +150 | °C   |
| P <sub>tot</sub> | total power dissipation  | T <sub>amb</sub> = -40 °C to +125 °C [2]                                | -    | 300  | mW   |

[1] The input and output voltage ratings may be exceeded if the input and output current ratings are observed.

[2] For TSSOP8 package: above 55 °C the value of P<sub>tot</sub> derates linearly with 2.5 mW/K.  
For VSSOP8 package: above 110 °C the value of P<sub>tot</sub> derates linearly with 8 mW/K.

## 9 Recommended operating conditions

**Table 6. Recommended operating conditions**

Voltages are referenced to GND (ground = 0 V).

| Symbol              | Parameter                           | Conditions              | 74HC3G34 |      |                 | 74HCT3G34 |      |                 | Unit |
|---------------------|-------------------------------------|-------------------------|----------|------|-----------------|-----------|------|-----------------|------|
|                     |                                     |                         | Min      | Typ  | Max             | Min       | Typ  | Max             |      |
| V <sub>CC</sub>     | supply voltage                      |                         | 2.0      | 5.0  | 6.0             | 4.5       | 5.0  | 5.5             | V    |
| V <sub>I</sub>      | input voltage                       |                         | 0        | -    | V <sub>CC</sub> | 0         | -    | V <sub>CC</sub> | V    |
| V <sub>O</sub>      | output voltage                      |                         | 0        | -    | V <sub>CC</sub> | 0         | -    | V <sub>CC</sub> | V    |
| T <sub>amb</sub>    | ambient temperature                 |                         | -40      | +25  | +125            | -40       | +25  | +125            | °C   |
| $\Delta t/\Delta V$ | input transition rise and fall rate | V <sub>CC</sub> = 2.0 V | -        | -    | 625             | -         | -    | -               | ns/V |
|                     |                                     | V <sub>CC</sub> = 4.5 V | -        | 1.67 | 139             | -         | 1.67 | 139             | ns/V |
|                     |                                     | V <sub>CC</sub> = 6.0 V | -        | -    | 83              | -         | -    | -               | ns/V |

## 10 Static characteristics

**Table 7. Static characteristics**

Voltages are referenced to GND (ground = 0 V).

| Symbol          | Parameter                 | Conditions                                                       | -40 °C to +85 °C |                    |      | -40 °C to +125 °C |      | Unit |
|-----------------|---------------------------|------------------------------------------------------------------|------------------|--------------------|------|-------------------|------|------|
|                 |                           |                                                                  | Min              | Typ <sup>[1]</sup> | Max  | Min               | Max  |      |
| 74HC3G34        |                           |                                                                  |                  |                    |      |                   |      |      |
| V <sub>IH</sub> | HIGH-level input voltage  | V <sub>CC</sub> = 2.0 V                                          | 1.5              | 1.2                | -    | 1.5               | -    | V    |
|                 |                           | V <sub>CC</sub> = 4.5 V                                          | 3.15             | 2.4                | -    | 3.15              | -    | V    |
|                 |                           | V <sub>CC</sub> = 6.0 V                                          | 4.2              | 3.2                | -    | 4.2               | -    | V    |
| V <sub>IL</sub> | LOW-level input voltage   | V <sub>CC</sub> = 2.0 V                                          | -                | 0.8                | 0.5  | -                 | 0.5  | V    |
|                 |                           | V <sub>CC</sub> = 4.5 V                                          | -                | 2.1                | 1.35 | -                 | 1.35 | V    |
|                 |                           | V <sub>CC</sub> = 6.0 V                                          | -                | 2.8                | 1.8  | -                 | 1.8  | V    |
| V <sub>OH</sub> | HIGH-level output voltage | V <sub>I</sub> = V <sub>IH</sub> or V <sub>IL</sub>              |                  |                    |      |                   |      |      |
|                 |                           | I <sub>O</sub> = -20 µA; V <sub>CC</sub> = 2.0 V                 | 1.9              | 2.0                | -    | 1.9               | -    | V    |
|                 |                           | I <sub>O</sub> = -20 µA; V <sub>CC</sub> = 4.5 V                 | 4.4              | 4.5                | -    | 4.4               | -    | V    |
|                 |                           | I <sub>O</sub> = -20 µA; V <sub>CC</sub> = 6.0 V                 | 5.9              | 6.0                | -    | 5.9               | -    | V    |
|                 |                           | I <sub>O</sub> = -4.0 mA; V <sub>CC</sub> = 4.5 V                | 4.13             | 4.32               | -    | 3.7               | -    | V    |
|                 |                           | I <sub>O</sub> = -5.2 mA; V <sub>CC</sub> = 6.0 V                | 5.63             | 5.81               | -    | 5.2               | -    | V    |
| V <sub>OL</sub> | LOW-level output voltage  | V <sub>I</sub> = V <sub>IH</sub> or V <sub>IL</sub>              |                  |                    |      |                   |      |      |
|                 |                           | I <sub>O</sub> = 20 µA; V <sub>CC</sub> = 2.0 V                  | -                | 0                  | 0.1  | -                 | 0.1  | V    |
|                 |                           | I <sub>O</sub> = 20 µA; V <sub>CC</sub> = 4.5 V                  | -                | 0                  | 0.1  | -                 | 0.1  | V    |
|                 |                           | I <sub>O</sub> = 20 µA; V <sub>CC</sub> = 6.0 V                  | -                | 0                  | 0.1  | -                 | 0.1  | V    |
|                 |                           | I <sub>O</sub> = 4.0 mA; V <sub>CC</sub> = 4.5 V                 | -                | 0.15               | 0.33 | -                 | 0.4  | V    |
|                 |                           | I <sub>O</sub> = 5.2 mA; V <sub>CC</sub> = 6.0 V                 | -                | 0.16               | 0.33 | -                 | 0.4  | V    |
| I <sub>I</sub>  | input leakage current     | V <sub>I</sub> = V <sub>CC</sub> or GND; V <sub>CC</sub> = 6.0 V | -                | -                  | ±1.0 | -                 | ±1.0 | µA   |

| Symbol           | Parameter                 | Conditions                                                                                                  | -40 °C to +85 °C |                    |      | -40 °C to +125 °C |      | Unit |
|------------------|---------------------------|-------------------------------------------------------------------------------------------------------------|------------------|--------------------|------|-------------------|------|------|
|                  |                           |                                                                                                             | Min              | Typ <sup>[1]</sup> | Max  | Min               | Max  |      |
| I <sub>CC</sub>  | supply current            | per input pin; V <sub>I</sub> = V <sub>CC</sub> or GND; I <sub>O</sub> = 0 A; V <sub>CC</sub> = 6.0 V       | -                | -                  | 10   | -                 | 20   | µA   |
| C <sub>I</sub>   | input capacitance         |                                                                                                             | -                | 1.5                | -    | -                 | -    | pF   |
| <b>74HCT3G34</b> |                           |                                                                                                             |                  |                    |      |                   |      |      |
| V <sub>IH</sub>  | HIGH-level input voltage  | V <sub>CC</sub> = 4.5 V to 5.5 V                                                                            | 2.0              | 1.6                | -    | 2.0               | -    | V    |
| V <sub>IL</sub>  | LOW-level input voltage   | V <sub>CC</sub> = 4.5 V to 5.5 V                                                                            | -                | 1.2                | 0.8  | -                 | 0.8  | V    |
| V <sub>OH</sub>  | HIGH-level output voltage | V <sub>I</sub> = V <sub>IH</sub> or V <sub>IL</sub>                                                         |                  |                    |      |                   |      |      |
|                  |                           | I <sub>O</sub> = -20 µA; V <sub>CC</sub> = 4.5 V                                                            | 4.4              | 4.5                | -    | 4.4               | -    | V    |
|                  |                           | I <sub>O</sub> = -4.0 mA; V <sub>CC</sub> = 4.5 V                                                           | 4.13             | 4.32               | -    | 3.7               | -    | V    |
| V <sub>OL</sub>  | LOW-level output voltage  | V <sub>I</sub> = V <sub>IH</sub> or V <sub>IL</sub>                                                         |                  |                    |      |                   |      |      |
|                  |                           | I <sub>O</sub> = 20 µA; V <sub>CC</sub> = 4.5 V                                                             | -                | 0                  | 0.1  | -                 | 0.1  | V    |
|                  |                           | I <sub>O</sub> = 4.0 mA; V <sub>CC</sub> = 4.5 V                                                            | -                | 0.15               | 0.33 | -                 | 0.4  | V    |
| I <sub>I</sub>   | input leakage current     | V <sub>I</sub> = V <sub>CC</sub> or GND; V <sub>CC</sub> = 5.5 V                                            | -                | -                  | ±1.0 | -                 | ±1.0 | µA   |
| I <sub>CC</sub>  | supply current            | V <sub>I</sub> = V <sub>CC</sub> or GND; I <sub>O</sub> = 0 A; V <sub>CC</sub> = 5.5 V                      | -                | -                  | 10   | -                 | 20   | µA   |
| ΔI <sub>CC</sub> | additional supply current | per input; V <sub>CC</sub> = 4.5 V to 5.5 V; V <sub>I</sub> = V <sub>CC</sub> - 2.1 V; I <sub>O</sub> = 0 A | -                | -                  | 375  | -                 | 410  | µA   |
| C <sub>I</sub>   | input capacitance         |                                                                                                             | -                | 1.5                | -    | -                 | -    | pF   |

[1] All typical values are measured at T<sub>amb</sub> = 25 °C.

## 11 Dynamic characteristics

**Table 8. Dynamic characteristics**

Voltages are referenced to GND (ground = 0 V); for test circuit see [Figure 5](#).

| Symbol    Parameter |                               | Conditions                                             | -40 °C to +85 °C |                    |     | -40 °C to +125 °C |     | Unit |
|---------------------|-------------------------------|--------------------------------------------------------|------------------|--------------------|-----|-------------------|-----|------|
|                     |                               |                                                        | Min              | Typ <sup>[1]</sup> | Max | Min               | Max |      |
| 74HC3G34            |                               |                                                        |                  |                    |     |                   |     |      |
| t <sub>pd</sub>     | propagation delay             | nA to nY; see <a href="#">Figure 4</a> <sup>[2]</sup>  |                  |                    |     |                   |     |      |
|                     |                               | V <sub>CC</sub> = 2.0 V                                | -                | 29                 | 95  | -                 | 125 | ns   |
|                     |                               | V <sub>CC</sub> = 4.5 V                                | -                | 9                  | 19  | -                 | 25  | ns   |
|                     |                               | V <sub>CC</sub> = 6.0 V                                | -                | 8                  | 16  | -                 | 20  | ns   |
| t <sub>t</sub>      | transition time               | nY; see <a href="#">Figure 4</a> <sup>[3]</sup>        |                  |                    |     |                   |     |      |
|                     |                               | V <sub>CC</sub> = 2.0 V                                | -                | 18                 | 95  | -                 | 125 | ns   |
|                     |                               | V <sub>CC</sub> = 4.5 V                                | -                | 6                  | 19  | -                 | 25  | ns   |
|                     |                               | V <sub>CC</sub> = 6.0 V                                | -                | 5                  | 16  | -                 | 20  | ns   |
| C <sub>PD</sub>     | power dissipation capacitance | V <sub>I</sub> = GND to V <sub>CC</sub> <sup>[4]</sup> | -                | 10                 | -   | -                 | -   | pF   |

| Symbol    Parameter |                               | Conditions                                                | -40 °C to +85 °C |                    |     | -40 °C to +125 °C |     | Unit |    |
|---------------------|-------------------------------|-----------------------------------------------------------|------------------|--------------------|-----|-------------------|-----|------|----|
|                     |                               |                                                           | Min              | Typ <sup>[1]</sup> | Max | Min               | Max |      |    |
| 74HCT3G34           |                               |                                                           |                  |                    |     |                   |     |      |    |
| t <sub>pd</sub>     | propagation delay             | nA to nY; see <a href="#">Figure 4</a>                    | [2]              |                    |     |                   |     |      |    |
|                     |                               | V <sub>CC</sub> = 4.5 V                                   |                  | -                  | 10  | 23                | -   | 29   | ns |
| t <sub>t</sub>      | transition time               | nY; V <sub>CC</sub> = 4.5 V; see <a href="#">Figure 4</a> | [3]              | -                  | 6   | 19                | -   | 25   | ns |
| C <sub>PD</sub>     | power dissipation capacitance | V <sub>I</sub> = GND to V <sub>CC</sub> - 1.5 V           | [4]              | -                  | 9   | -                 | -   | -    | pF |

[1] All typical values are measured at T<sub>amb</sub> = 25 °C.  
[2] t<sub>pd</sub> is the same as t<sub>PLH</sub> and t<sub>PHL</sub>.  
[3] t<sub>t</sub> is the same as t<sub>TLH</sub> and t<sub>THL</sub>.  
[4] C<sub>PD</sub> is used to determine the dynamic power dissipation (P<sub>D</sub> in μW).  
P<sub>D</sub> = C<sub>PD</sub> × V<sub>CC</sub><sup>2</sup> × f<sub>i</sub> × N + Σ(C<sub>L</sub> × V<sub>CC</sub><sup>2</sup> × f<sub>o</sub>) where:  
f<sub>i</sub> = input frequency in MHz;  
f<sub>o</sub> = output frequency in MHz;  
C<sub>L</sub> = output load capacitance in pF;  
V<sub>CC</sub> = supply voltage in V;  
N = number of inputs switching;  
Σ(C<sub>L</sub> × V<sub>CC</sub><sup>2</sup> × f<sub>o</sub>) = sum of outputs.

11.1 Waveform and test circuit

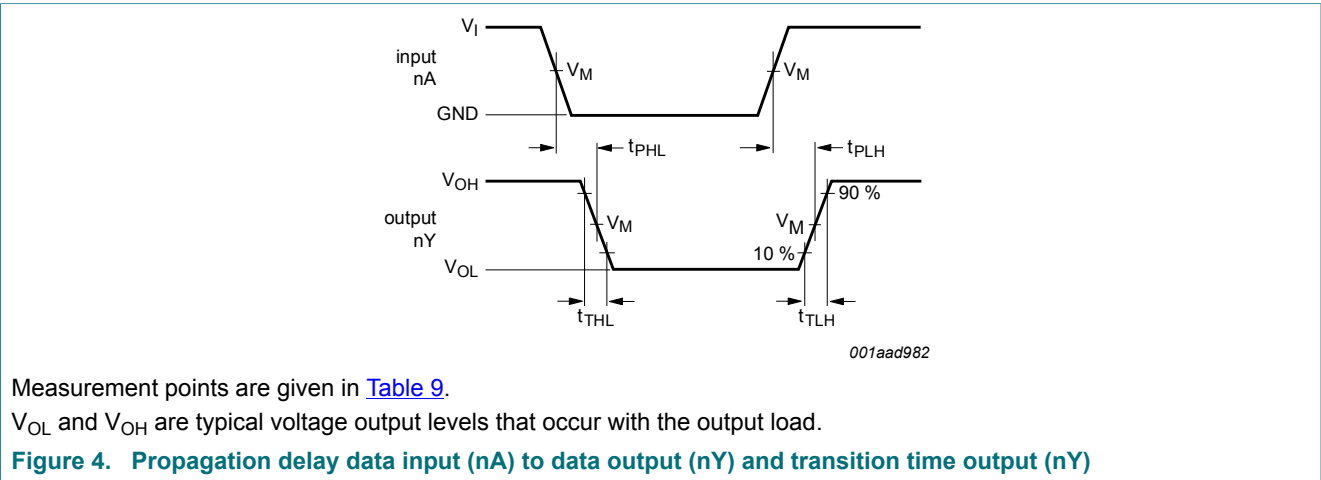


Table 9. Measurement points

| Type      | Input                 | Output                |
|-----------|-----------------------|-----------------------|
|           | V <sub>M</sub>        | V <sub>M</sub>        |
| 74HC3G34  | 0.5 × V <sub>CC</sub> | 0.5 × V <sub>CC</sub> |
| 74HCT3G34 | 1.3 V                 | 1.3 V                 |

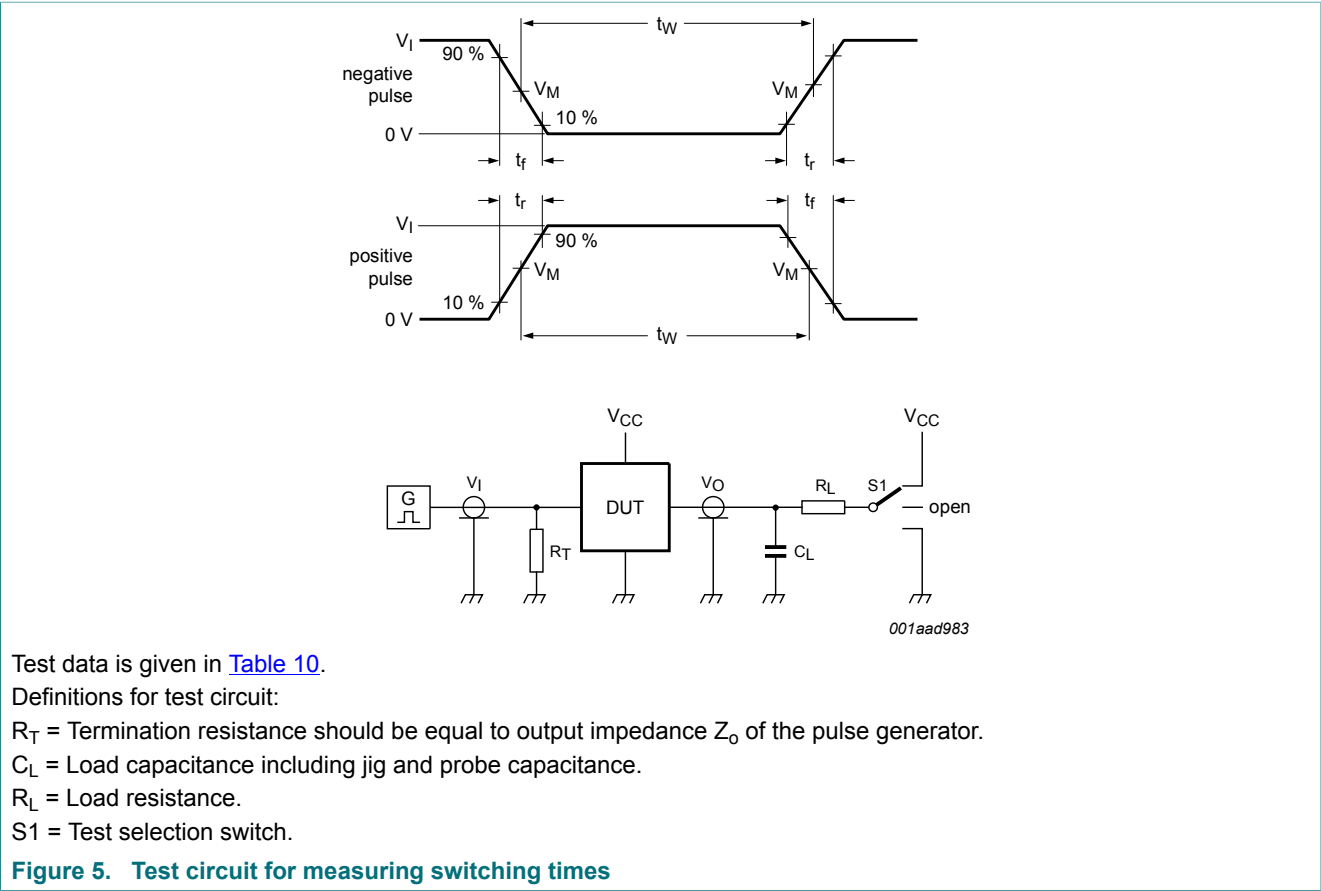
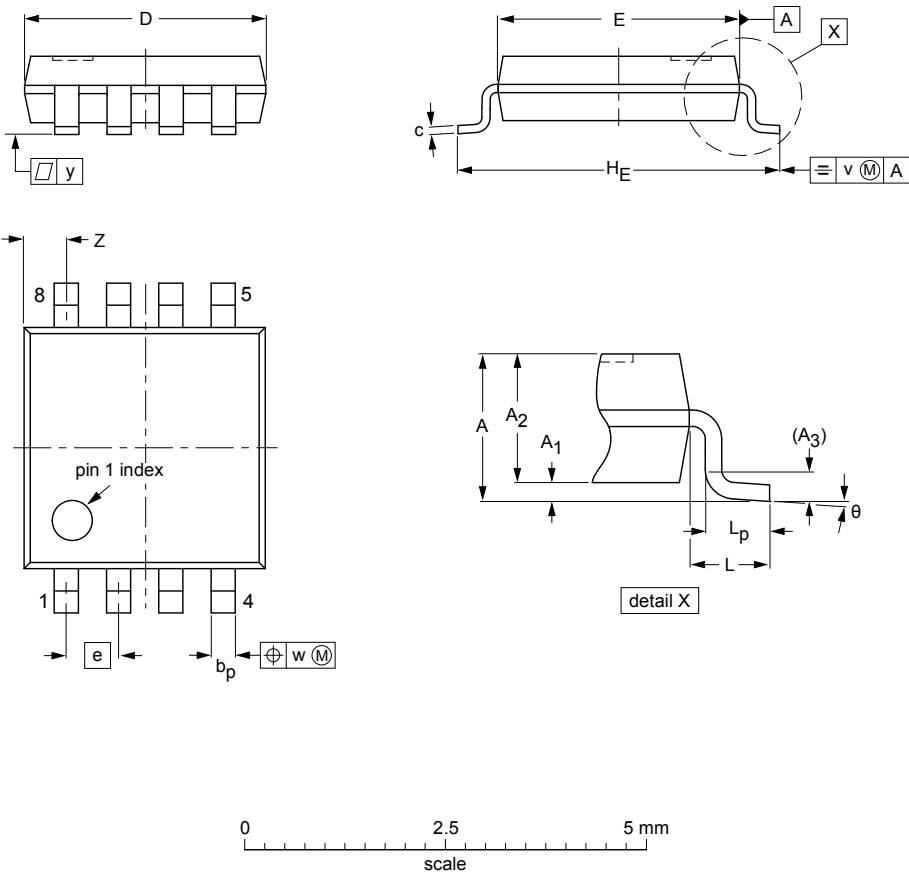


Table 10. Test data

| Type      | Input           |             | Load  |              | S1 position        |
|-----------|-----------------|-------------|-------|--------------|--------------------|
|           | $V_I$           | $t_r, t_f$  | $C_L$ | $R_L$        | $t_{PHL}, t_{PLH}$ |
| 74HC3G34  | GND to $V_{CC}$ | $\leq 6$ ns | 50 pF | 1 k $\Omega$ | open               |
| 74HCT3G34 | GND to 3 V      | $\leq 6$ ns | 50 pF | 1 k $\Omega$ | open               |

12 Package outline

TSSOP8: plastic thin shrink small outline package; 8 leads; body width 3 mm; lead length 0.5 mm SOT505-2



DIMENSIONS (mm are the original dimensions)

| UNIT | A<br>max. | A <sub>1</sub> | A <sub>2</sub> | A <sub>3</sub> | b <sub>p</sub> | c            | D <sup>(1)</sup> | E <sup>(1)</sup> | e    | H <sub>E</sub> | L   | L <sub>p</sub> | v   | w    | y   | Z <sup>(1)</sup> | θ        |
|------|-----------|----------------|----------------|----------------|----------------|--------------|------------------|------------------|------|----------------|-----|----------------|-----|------|-----|------------------|----------|
| mm   | 1.1       | 0.15<br>0.00   | 0.95<br>0.75   | 0.25           | 0.38<br>0.22   | 0.18<br>0.08 | 3.1<br>2.9       | 3.1<br>2.9       | 0.65 | 4.1<br>3.9     | 0.5 | 0.47<br>0.33   | 0.2 | 0.13 | 0.1 | 0.70<br>0.35     | 8°<br>0° |

Note  
1. Plastic or metal protrusions of 0.15 mm maximum per side are not included.

| OUTLINE<br>VERSION | REFERENCES |       |       |  | EUROPEAN<br>PROJECTION | ISSUE DATE |
|--------------------|------------|-------|-------|--|------------------------|------------|
|                    | IEC        | JEDEC | JEITA |  |                        |            |
| SOT505-2           |            | ---   |       |  |                        | 02-01-16   |

Figure 6. Package outline SOT505-2 (TSSOP8)

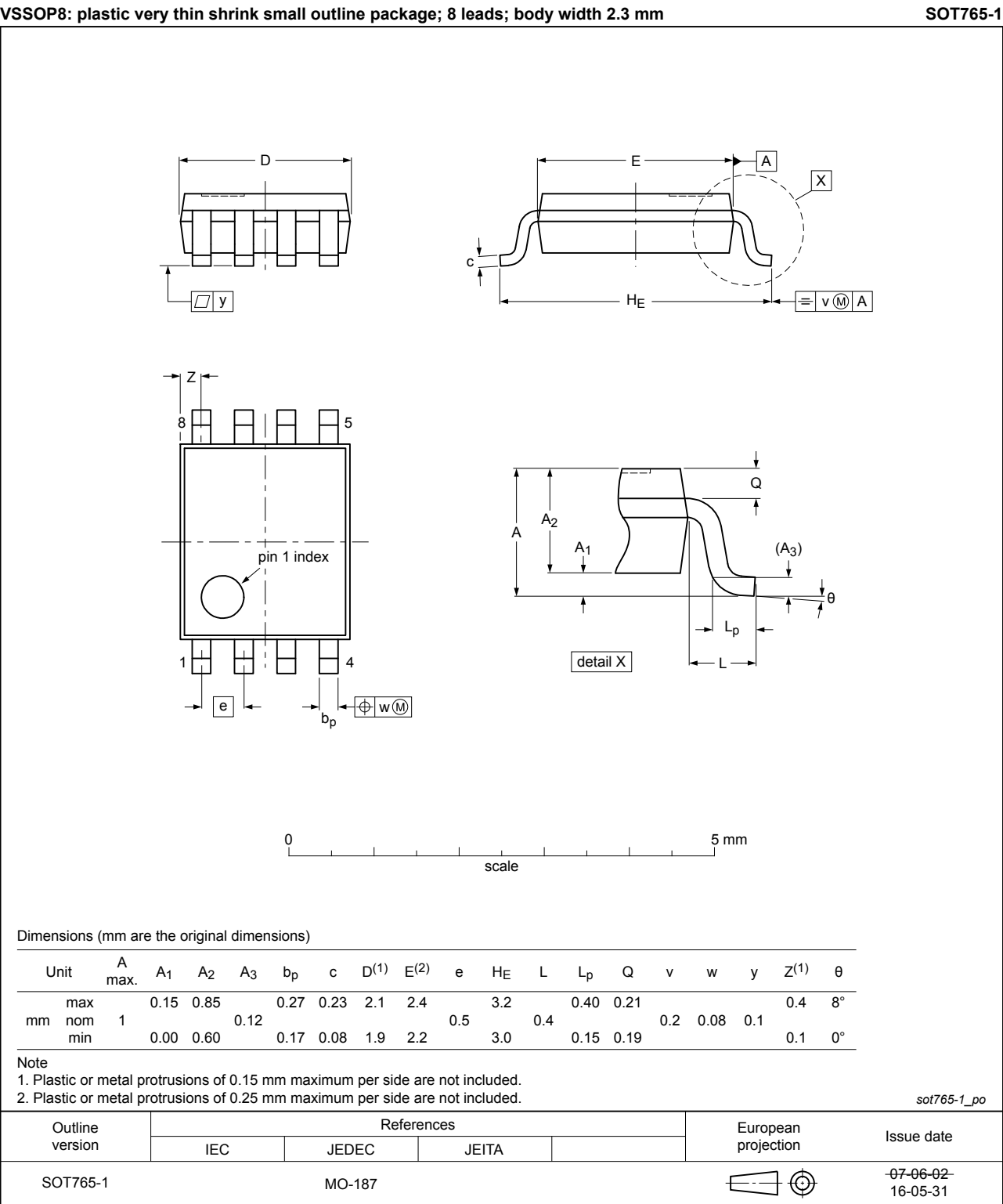


Figure 7. Package outline SOT765-1 (VSSOP8)

## 13 Abbreviations

Table 11. Abbreviations

| Acronym | Description                             |
|---------|-----------------------------------------|
| CMOS    | Complementary Metal Oxide Semiconductor |
| DUT     | Device Under Test                       |
| ESD     | ElectroStatic Discharge                 |
| HBM     | Human Body Model                        |
| MM      | Machine Model                           |
| TTL     | Transistor-Transistor Logic             |

## 14 Revision history

Table 12. Revision history

| Document ID      | Release date                                                                                                                                                                                                                                                                                        | Data sheet status     | Change notice | Supersedes       |
|------------------|-----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|-----------------------|---------------|------------------|
| 74HC_HCT3G34 v.7 | 20180611                                                                                                                                                                                                                                                                                            | Product data sheet    | -             | 74HC_HCT3G34 v.6 |
| Modifications:   | <ul style="list-style-type: none"> <li>The format of this data sheet has been redesigned to comply with the identity guidelines of Nexperia.</li> <li>Legal texts have been adapted to the new company name where appropriate.</li> <li>Type numbers 74HC3G34GD and 74HCT3G34GD removed.</li> </ul> |                       |               |                  |
| 74HC_HCT3G34 v.6 | 20131211                                                                                                                                                                                                                                                                                            | Product data sheet    | -             | 74HC_HCT3G34 v.5 |
| Modifications:   | <ul style="list-style-type: none"> <li>For type numbers 74HC3G34GD and 74HCT3G34GD XSON8U has changed to XSON8.</li> </ul>                                                                                                                                                                          |                       |               |                  |
| 74HC_HCT3G34 v.5 | 20090507                                                                                                                                                                                                                                                                                            | Product data sheet    | -             | 74HC_HCT3G34 v.4 |
| 74HC_HCT3G34 v.4 | 20060309                                                                                                                                                                                                                                                                                            | Product data sheet    | -             | 74HC_HCT3G34 v.3 |
| 74HC_HCT3G34 v.3 | 20030519                                                                                                                                                                                                                                                                                            | Product specification | -             | 74HC_HCT3G34 v.2 |
| 74HC_HCT3G34 v.2 | 20030210                                                                                                                                                                                                                                                                                            | Product specification | -             | 74HC_HCT3G34 v.1 |
| 74HC_HCT3G34 v.1 | 20031003                                                                                                                                                                                                                                                                                            | Product specification | -             | -                |

## 15 Legal information

### 15.1 Data sheet status

| Document status <sup>[1][2]</sup> | Product status <sup>[3]</sup> | Definition                                                                            |
|-----------------------------------|-------------------------------|---------------------------------------------------------------------------------------|
| Objective [short] data sheet      | Development                   | This document contains data from the objective specification for product development. |
| Preliminary [short] data sheet    | Qualification                 | This document contains data from the preliminary specification.                       |
| Product [short] data sheet        | Production                    | This document contains the product specification.                                     |

[1] Please consult the most recently issued document before initiating or completing a design.

[2] The term 'short data sheet' is explained in section "Definitions".

[3] The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the Internet at URL <http://www.nexperia.com>.

### 15.2 Definitions

**Draft** — The document is a draft version only. The content is still under internal review and subject to formal approval, which may result in modifications or additions. Nexperia does not give any representations or warranties as to the accuracy or completeness of information included herein and shall have no liability for the consequences of use of such information.

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Contents

1 General description ..... 1

2 Features and benefits .....1

3 Ordering information ..... 1

4 Marking .....2

5 Functional diagram .....2

6 Pinning information ..... 2

6.1 Pinning .....2

6.2 Pin description ..... 3

7 Functional description .....3

8 Limiting values .....3

9 Recommended operating conditions .....4

10 Static characteristics .....4

11 Dynamic characteristics .....5

11.1 Waveform and test circuit .....6

12 Package outline .....8

13 Abbreviations ..... 10

14 Revision history ..... 10

15 Legal information ..... 11

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